

OBSOLETE PRODUCT

OBSOLETE PRODUCT

NO RECOMMENDED REPLACEMENT

Support Center at Support Center at Contact our Technical Support Center at Contact our Technical Support Center at Contact Or Www.intersil.com/tsc

1-888-INTERSIL or Www.intersil.com/tsc

January 1996, Rev B

FN7289

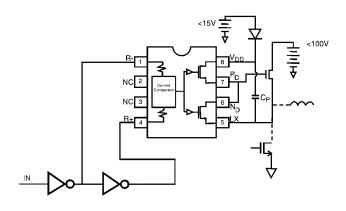
100V High Side Driver

élantec.

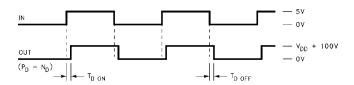
The EL7501 provides a low cost solution to many high side drive applications. The EL7501 is DC

coupled so there are no start up problems associated with AC coupled schemes. The EL7501 is driven by user supplied complementary signals.

Pinout



7501 Waveform Example



Features

- 100V High Side Voltage
- · Rail to Rail Output
- 1MHz Operation
- 1.0A Peak Current
- · Matched Rise and Fall Times
- Direct Coupled
- · No Start Up Ambiguity

Applications

- Uninterruptible Power Supplies
- DC-DC Converters
- Motor Control
- Power MOSFET Driver

Ordering Information

PART NUMBER	TEMP. RANGE	PACKAGE	PKG. NO.
EL7501CN	-40°C to +85°C	8-Pin PDIP	MDP0031
EL7501CS	-40°C to +85°C	8-Pin SO	MDP0027

EL7501

Absolute Maximum Ratings (T_A = 25°C)

Supply (V _{DD} or LX to R- or R+)100V	Ambient Operating Temperature
Supply (V _{DD} to LX)	Storage Temperature Range65°C to +150°C
Output Pins0.3V below GND,	Operating Junction Temperature
+0.3V above V _{DD}	Power Dissipation SOIC570mW
Peak Output Current	PDIP1050mW

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

$\textbf{DC Electrical Specifications} \hspace{0.5cm} \textbf{T}_{A} = 25^{\circ} \textbf{C}, \ \textbf{V}_{DD} = 15 \textbf{V}, \ \textbf{C}_{LOAD} = 1000 p \textbf{F}, \ unless otherwise specified}.$

PARAM	ETER	DESCRIPTION	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Input/Output	V _{DIFF (Min)}	Minimum Differential Input Signal to Switch Output		1.0			V
	I _{DS OFF}	Output Leakage	GND < V _{OUT} < V _{DD}	-10.0	0.2	+10.0	μA
	R _{OH}	Pull-up Resistance	I _{OUT} = -100mA		5.0	10.0	Ω
	R _{OL}	Pull-down Resistance	I _{OUT} = +100mA		5.0	10.0	Ω
	I _{PK}	Peak Output Current			1.0		Α
	I _{DC}	Continuous Output Current Source/Sink		50.0			mA
Power Supply	I _{DD}	Supply Current into V _{DD}				4.0	mA
	V_{DD}	Operating Voltage		4.5		15.0	V

$\textbf{AC Electrical Specifications} \qquad \text{T}_{A} = 25 ^{\circ}\text{C}, \ \text{V}_{DD} = 15 \text{V}, \ \text{C}_{LOAD} = 1000 \text{pF}, \ \text{unless otherwise specified}.$

PARAME	ΓER	DESCRIPTION	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Switching Characteristics	t _R	Rise Time	C _L = 500pF C _L = 1000pF		15.0 20.0	40.0	ns
	t _F	Fall Time	C _L = 500pF C _L = 1000pF		15.0 20.0	40.0	ns
	t _{D OFF}	Turn Off Delay Time			90.0	140.0	ns
	t _D ON	Turn On Delay Time			90.0	140.0	ns

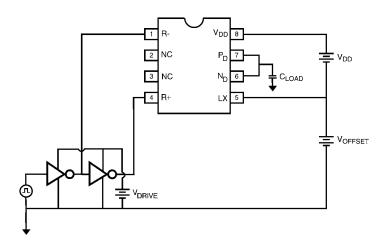


FIGURE 1. EL7501 TEST CIRCUIT

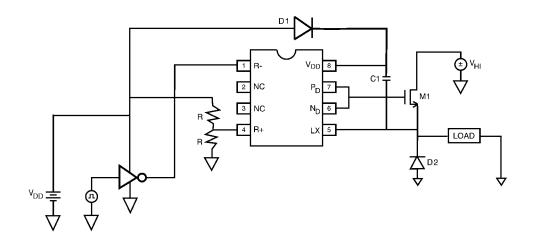
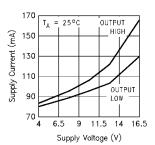


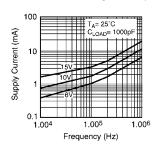
FIGURE 2. EL7501 ALTERNATE DRIVE METHOD

Typical Performance Curves

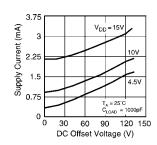
Quiescent Supply Current vs Supply Voltage



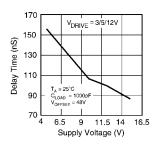
Average Supply Current vs Voltage and Frequency



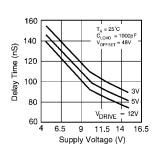
Supply Current vs DC Offset Voltage and V_{DD}



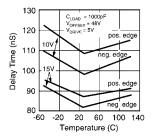
Output Rising Edge Delay Time vs Supply Voltage and Drive Voltage



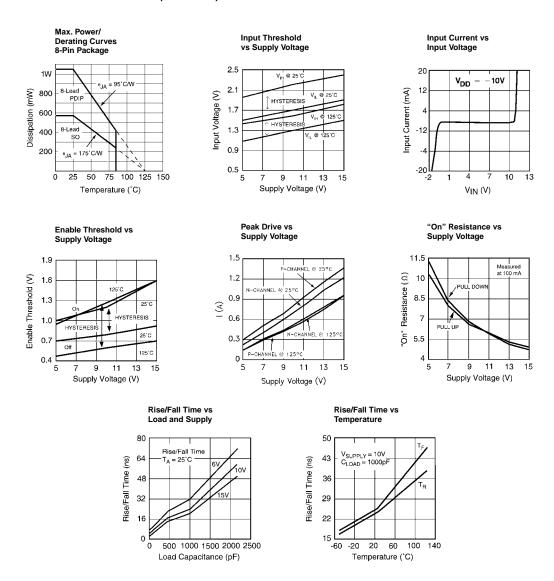
Output Falling Edge Delay Time vs Supply Voltage and Drive Voltage



Delay Time vs Temperature and Supply Voltage



Typical Performance Curves (Continued)



All Intersil U.S. products are manufactured, assembled and tested utilizing ISO9000 quality systems. Intersil Corporation's quality certifications can be viewed at www.intersil.com/design/quality

Intersil products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design, software and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see www.intersil.com